



066340.0212 (A36393 PCT USA)  
PATENT

*JW*

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Uwe et al.  
Serial No. : 10/511,575 Examiner : (Not Yet Assigned)  
Filed : October 13, 2004 Group Art Unit: (Not Yet Assigned)  
For : METHOD AND ARRANGEMENT FOR POWER EFFICIENT  
CONTROL OF PROCESSORS

STATUS LETTER

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

August 16, 2006

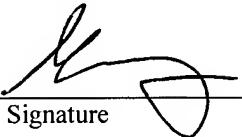
Date of Deposit

Gary M. Butter

Attorney Name

33,841

PTO Registration No.

  
Signature

August 16, 2006

Date of Signature

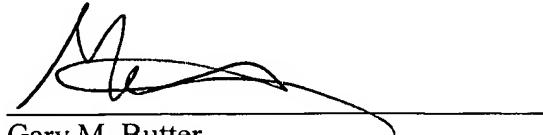
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Sir:

The last correspondence received on June 23, 2005 from the United States Patent and Trademark Office for the above-referenced application was the Notice of Recordation of Assignment document.

Please indicate when we may expect to receive an Office Action by completing the lower portion of this letter and returning it to the applicant in the enclosed stamped, self-addressed envelope.

Respectfully submitted,



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Enclosure

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*The First Office Action should be issued by \_\_\_\_\_.*

*The First Office Action was sent on \_\_\_\_\_.*